



JUN 23 2004

TECH CENTER 1.600/2900

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Docket No.: M4065.0424/P424  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
William E. Corr

Application No.: 09/808,140

Group Art Unit: 2858

Filed: March 15, 2001

Examiner: A. K. Deb

For: CIRCUIT FOR MEASURING ON-CHIP  
POWER SUPPLY INTEGRITY

**RESPONSE TO RESTRICTION REQUIREMENT**


Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed May 20, 2004, Applicant hereby elects the invention defined by Group I, claims 1, 3-33, 35-47 and 49-52, without traverse. Favorable action on the application is solicited.

Dated: June 18, 2004

Respectfully submitted,

By   
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